



5/6

```

0167: ##### SP_DCSQ 4 = 0x4
0167: 01 5F # SP_DCSQ_STM => start frame: 120
0169: 01 82 # SP_NXT_DCSQ_SA = 386 = 0x182
016B: 07 00 10 # CHG_COLON (Extended field size = 16 = 0x10)
016E: 00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
0172: 00 6E 10 10 FF FF # PX_CTLI 0: cspn=110 col= 0 1 0 1 cntr=F F F F
0178: 0F FF FF FF # LN_CTLI Termination Code
017C: 06 00 08 00 37 # SET_DSPXA uf=0x8 bf=0x37
0181: FF # CMD_END
0182: ##### SP_DCSQ 5 = 0x5
0182: 01 B7 # SP_DCSQ_STM => start frame: 150
0184: 01 9D # SP_NXT_DCSQ_SA = 413 = 0x19D
0186: 07 00 10 # CHG_COLON (Extended field size = 16 = 0x10)
0189: 00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
018D: 00 6C 10 10 FF FF # PX_CTLI 0: cspn=108 col= 0 1 0 1 cntr=F F F F

0193: 0F FF FF FF # LN_CTLI Termination Code
0197: 06 00 09 00 38 # SET_DSPXA uf=0x9 bf=0x38
019C: FF # CMD_END
019D: ##### SP_DCSQ 6 = 0x6
019D: 02 0F # SP_DCSQ_STM => start frame: 180
019F: 01 B8 # SP_NXT_DCSQ_SA = 440 = 0x1B8
01A1: 07 00 10 # CHG_COLON (Extended field size = 16 = 0x10)
01A4: 00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
01A8: 00 6A 10 10 FF FF # PX_CTLI 0: cspn=106 col= 0 1 0 1 cntr=F F F F
01AE: 0F FF FF FF # LN_CTLI Termination Code
01B2: 06 00 0A 00 39 # SET_DSPXA uf=0xA bf=0x39
01B7: FF # CMD_END
01B8: ##### SP_DCSQ 7 = 0x7
01B8: 02 67 # SP_DCSQ_STM => start frame: 210
01BA: 01 D3 # SP_NXT_DCSQ_SA = 467 = 0x1D3
01BC: 07 00 10 # CHG_COLON (Extended field size = 16 = 0x10)
01BF: 00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
01C3: 00 68 10 10 FF FF # PX_CTLI 0: cspn=104 col= 0 1 0 1 cntr=F F F F
01C9: 0F FF FF FF # LN_CTLI Termination Code
01CD: 06 00 0B 00 3A # SET_DSPXA uf=0xB bf=0x3A
01D2: FF # CMD_END
01D3: ##### SP_DCSQ 8 = 0x8
01D3: 02 BF # SP_DCSQ_STM => start frame: 240
01D5: 01 EE # SP_NXT_DCSQ_SA = 494 = 0x1EE
01D7: 07 00 10 # CHG_COLON (Extended field size = 16 = 0x10)
01DA: 00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
01DE: 00 66 10 10 FF FF # PX_CTLI 0: cspn=102 col= 0 1 0 1 cntr=F F F F
01E4: 0F FF FF FF # LN_CTLI Termination Code
01E8: 06 00 0C 00 3B # SET_DSPXA uf=0xC bf=0x3B
01ED: FF # CMD_END
01EE: ##### SP_DCSQ 9 = 0x9
01EE: 03 17 # SP_DCSQ_STM => start frame: 270
01F0: 01 EE # SP_NXT_DCSQ_SA = 494 = 0x1EE
01F2: 07 00 06 # CHG_COLON (Extended field size = 6 = 0x6)
01F5: 0F FF FF FF # LN_CTLI Termination Code
01F9: 02 # STP_DSP
01FA: FF # CMD_END
01FB: FF # Stuffing Byte => even SP size

```

FIG. 7